Personal Impressions from the Programme Definition Process – 1st ICT Work Programme

András Pataricza

Budapest University of Technology and Economics Dept. of Measurement and Information Systems

pataric@mit.bme.hu

Budapest University of Technology and Economics
└ **Faculty of Electrical Engineering and Informatics**
  └ **Dept. Measurement and Information Systems**
    - Artificial Intelligence
    - Embedded systems
    - **Fault Tolerant Systems**
      - **25+ members**
        - **2 associate professors**
        - **total of 6 members having a degree**
      - **2 spin-off companies**
        - **SearchLab** — security
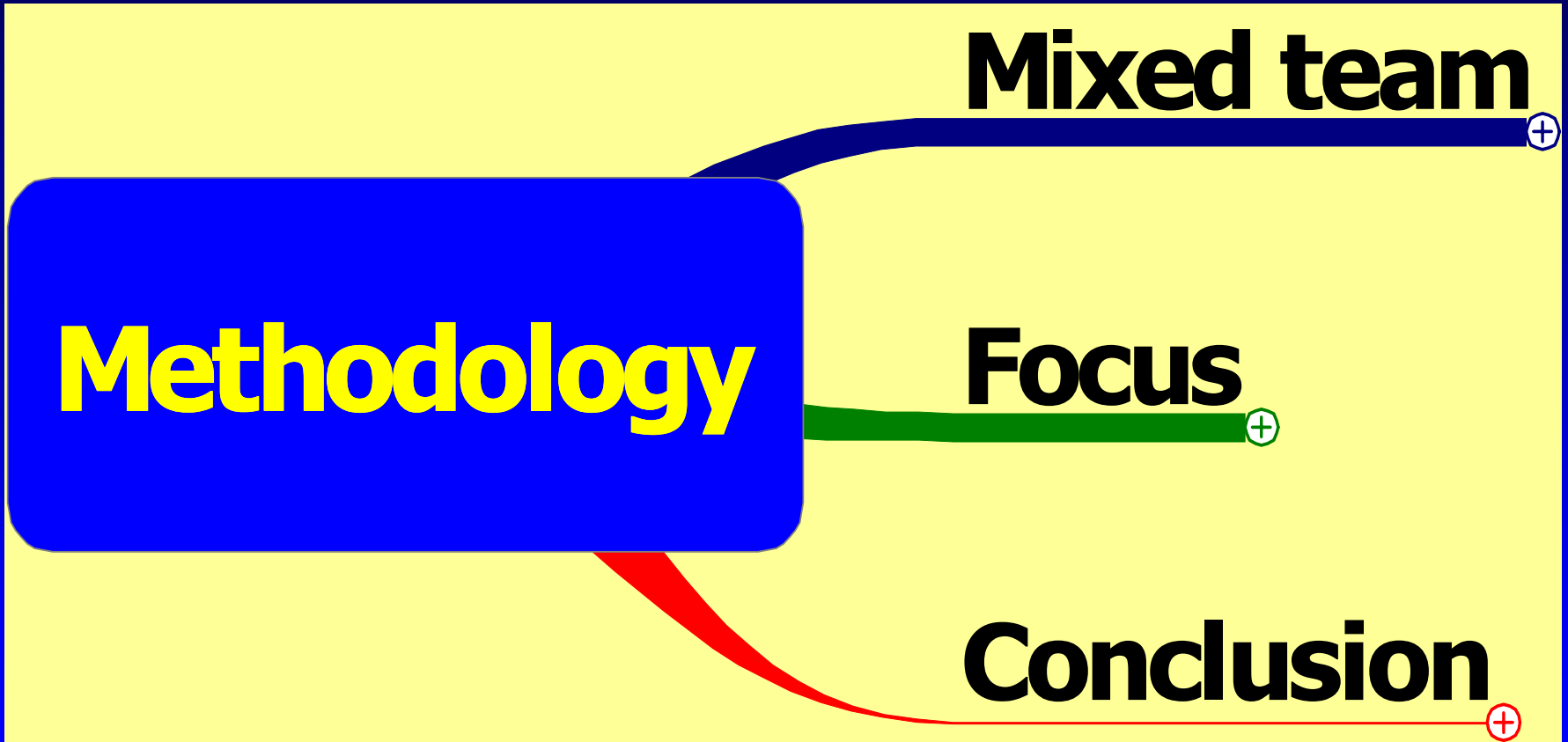   - **OptXware** — modelware / dependability consolidation
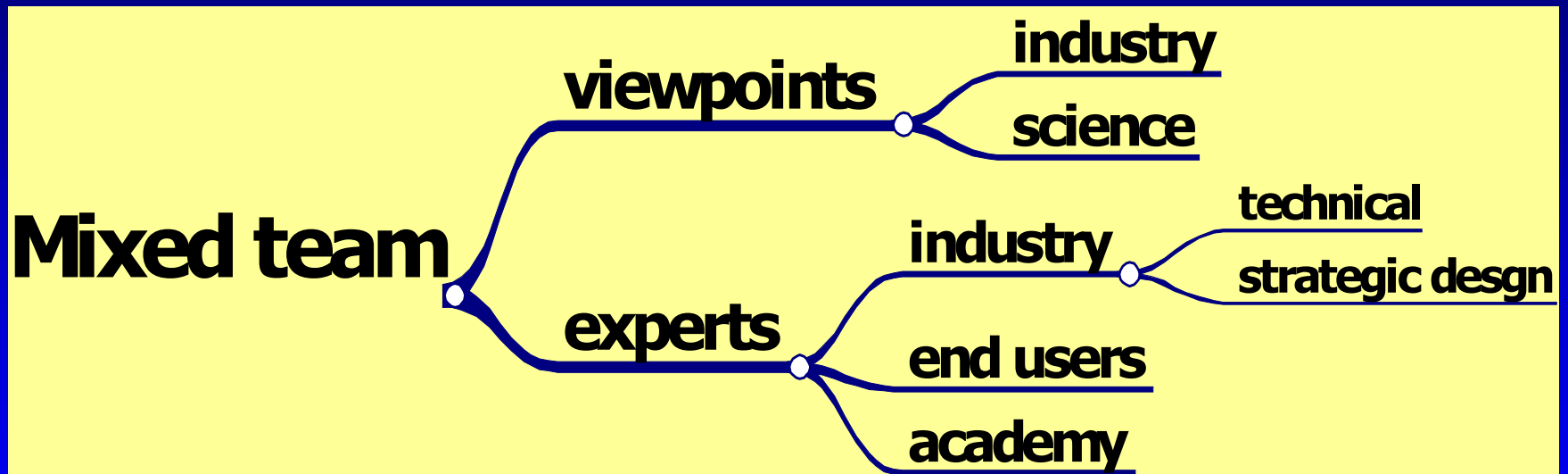
Representative IST projects

- **HIDENETS**
    Highly Dependable IP-based Networks and Services
        dynamic configurations

- **DESEREC**
    Dependability and Security by Enhanced Reconfigurability
        design of fast reactions

- **DECOS**
    Dependable Embedded Computer Systems

- SENSORIA
    Global Computing
        web services
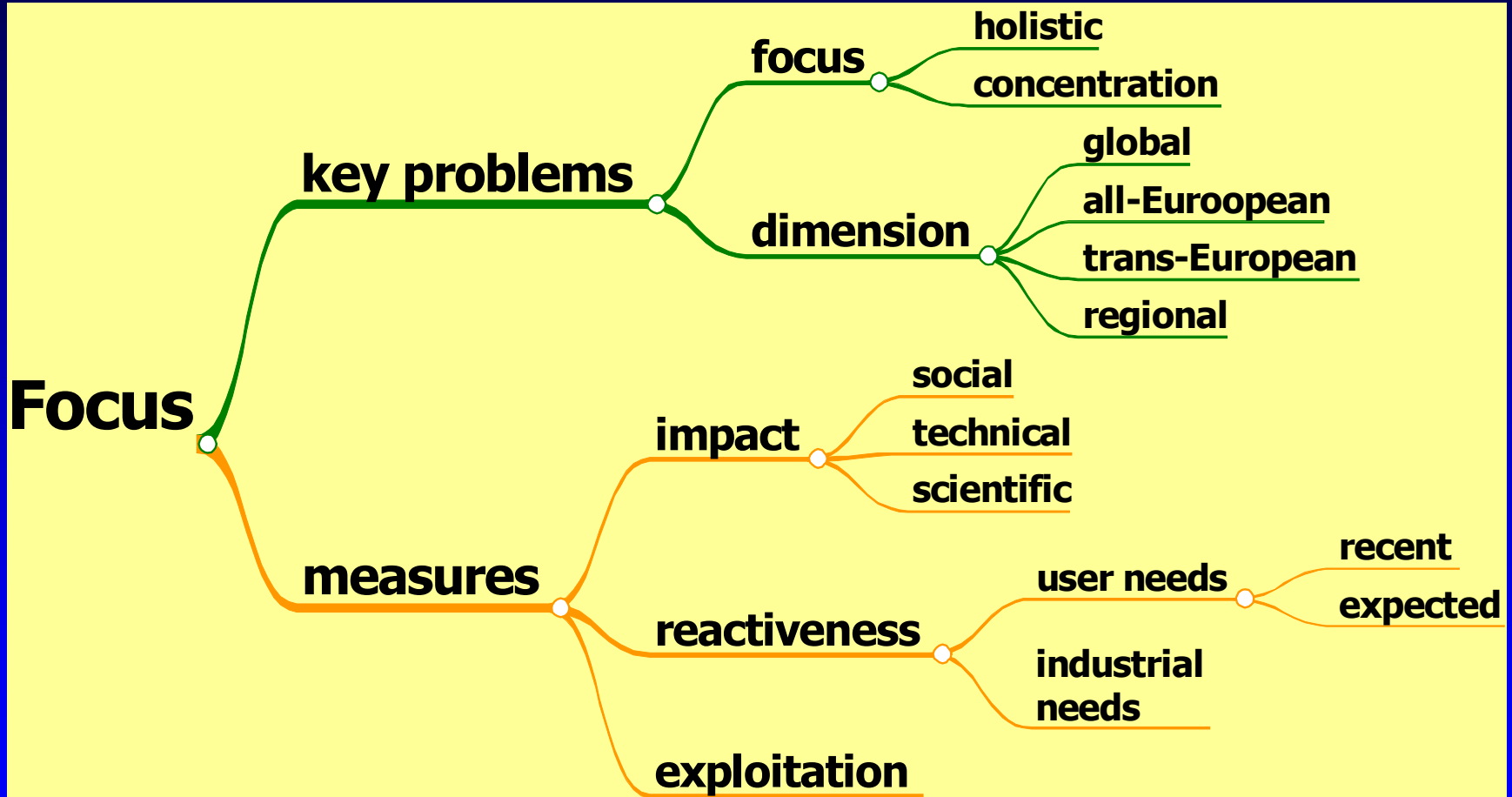
- DIANA

    Java in safety critical applications

Preparation meeting

Methodology

- **Mixed team** ⊕
- **Focus** ⊕
- **Conclusion** ⊕

Expert body composition

Approach

Fault Tolerant Systems
Research Group

Result

Conclusion

- **integration**
  - SOTA
  - evolution
  - existing IP
  - reuse
- Science as "intelligence provider"
- **harmonization**
  - technology
  - algorithms
  - standards